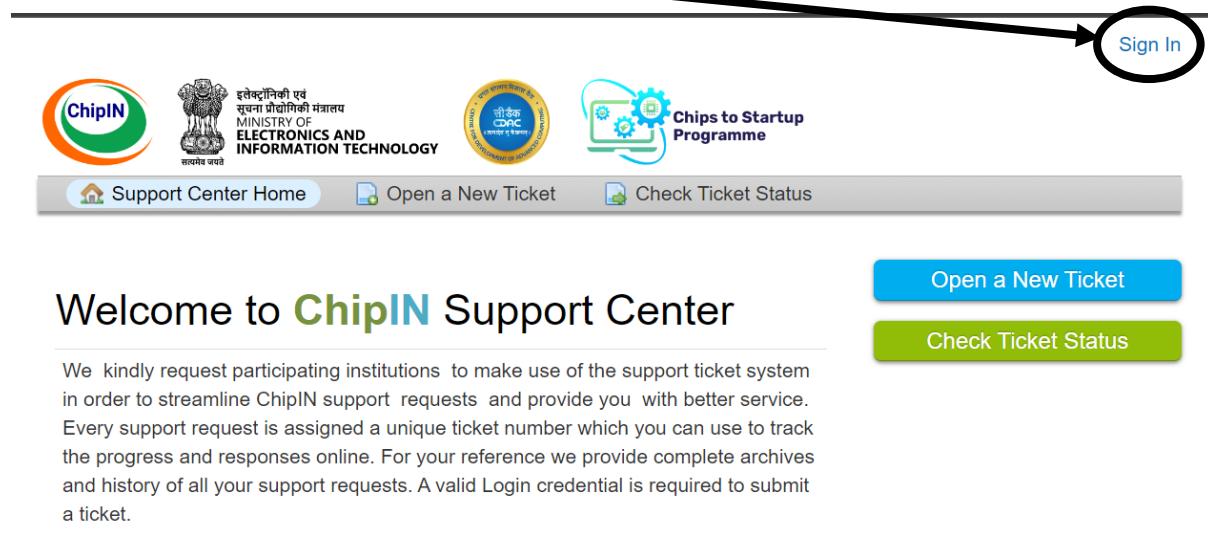


Procedure for Requesting Centralized Access to Advanced FPGA Boards (AMD/Xilinx Alveo U55C and Versal VCK5000)

Step-1: Log into <https://chipin.cdacb.in/index.php>

- Click on Sign-in button



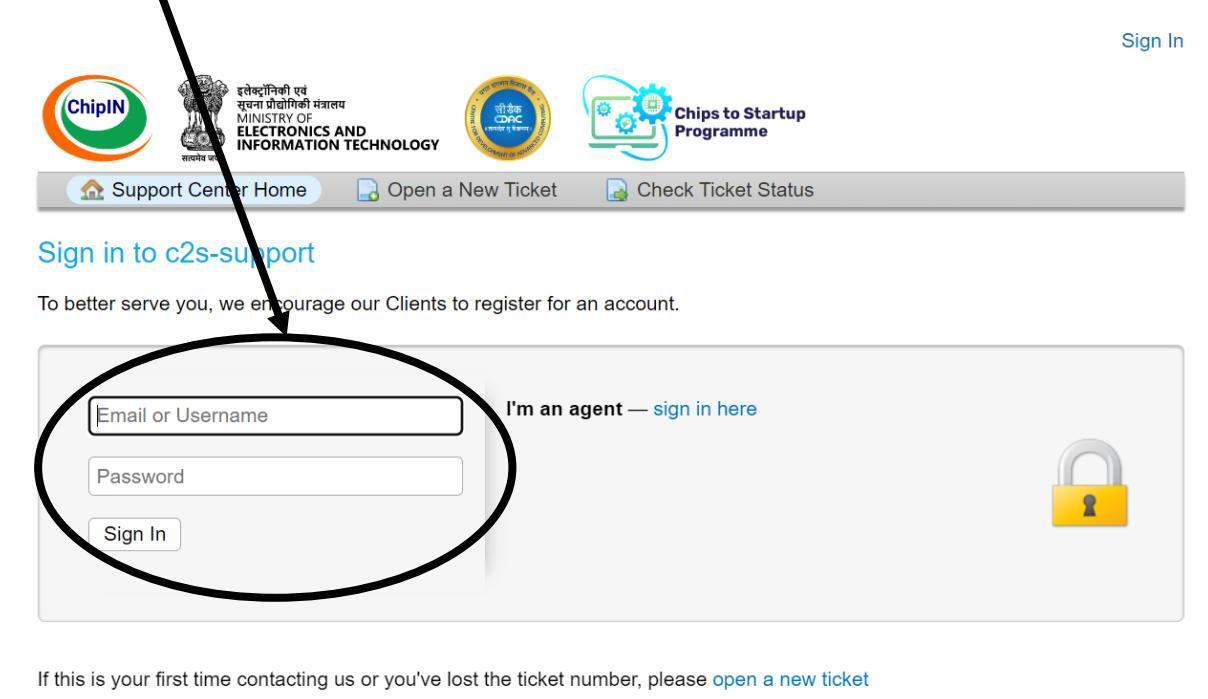
Welcome to **ChipIN** Support Center

We kindly request participating institutions to make use of the support ticket system in order to streamline ChipIN support requests and provide you with better service. Every support request is assigned a unique ticket number which you can use to track the progress and responses online. For your reference we provide complete archives and history of all your support requests. A valid Login credential is required to submit a ticket.

[Open a New Ticket](#)

[Check Ticket Status](#)

- Sign into chipin.cdacb.in using login credential provided to the Institute



[Sign in to c2s-support](#)

To better serve you, we encourage our Clients to register for an account.

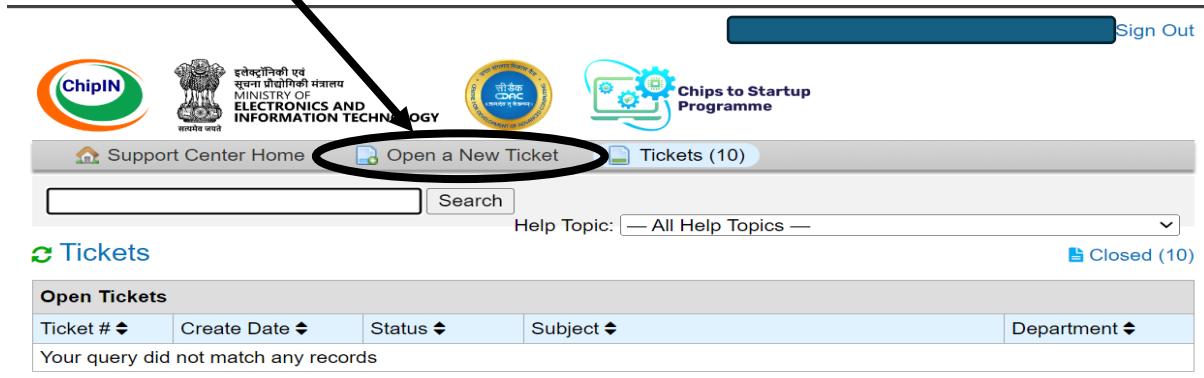
[I'm an agent — sign in here](#)



If this is your first time contacting us or you've lost the ticket number, please [open a new ticket](#)

Step-2: Create a new ticket requesting access to either one or both FPGA boards.

- Open a new ticket



Support Center Home **Open a New Ticket** Tickets (10)

Help Topic: — All Help Topics —

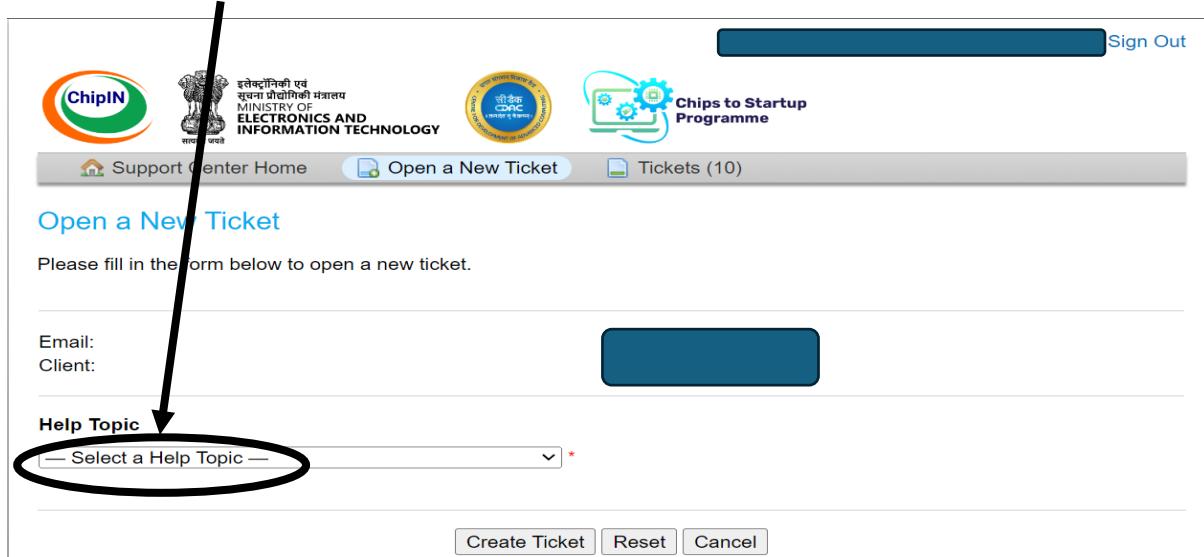
Tickets

Open Tickets

| Ticket # | Create Date | Status | Subject | Department |
|--------------------------------------|-------------|--------|---------|------------|
| Your query did not match any records | | | | |

Closed (10)

- Click on Select a Help Topic



Support Center Home **Open a New Ticket** Tickets (10)

Open a New Ticket

Please fill in the form below to open a new ticket.

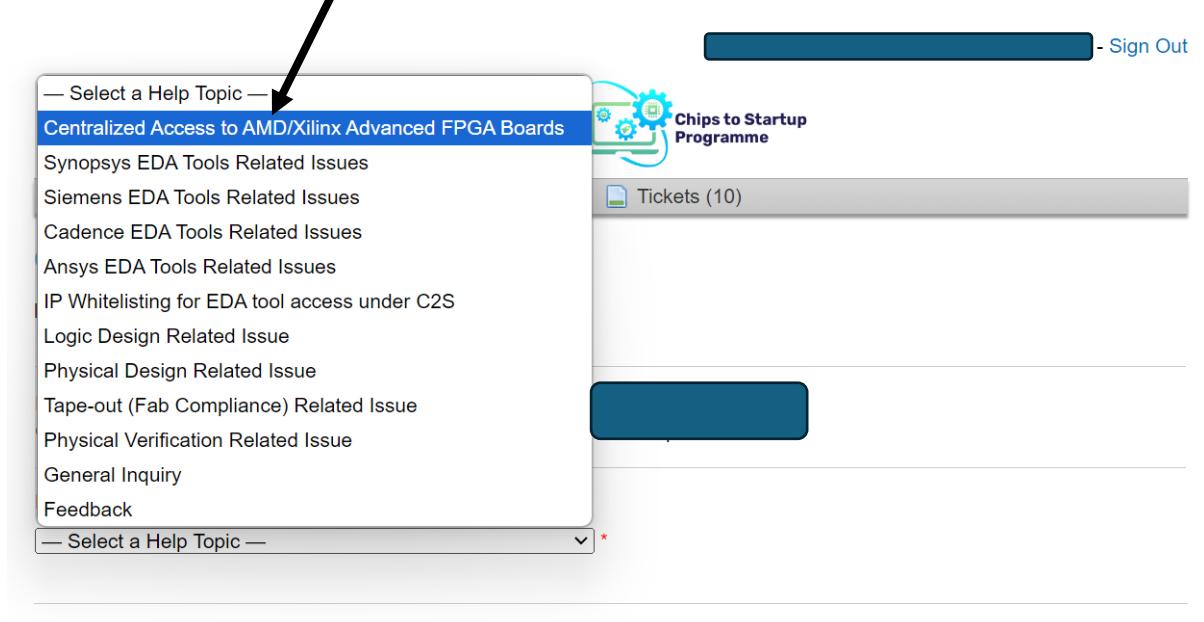
Email:

Client:

Help Topic **Select a Help Topic**

Create Ticket **Reset** **Cancel**

- Select 'Centralized Access to AMD/Xilinx Advanced FPGA Boards'



— Select a Help Topic —

Centralized Access to AMD/Xilinx Advanced FPGA Boards

Synopsys EDA Tools Related Issues

Siemens EDA Tools Related Issues

Cadence EDA Tools Related Issues

Ansys EDA Tools Related Issues

IP Whitelisting for EDA tool access under C2S

Logic Design Related Issue

Physical Design Related Issue

Tape-out (Fab Compliance) Related Issue

Physical Verification Related Issue

General Inquiry

Feedback

— Select a Help Topic —

Create Ticket **Reset** **Cancel**

- Enter the date on which you want access to the particular FPGA board or both boards.

Open a New Ticket

Please fill in the form below to open a new ticket.

Email:

Client:

Help Topic

Centralized AMD/Xilinx high-end FPGAs Boards Access *

Request for access to Alveo U55C or Versal VCK5000 AMD/Xilinx Development Boards

The institution must request access to the FPGA board on a specific date.

Request for Alveo U55C Development Board

 (IST)

Request for Versal VCK5000 Development Board

 (IST)

- Click on the calendar view and select the date

Support Center Home Tickets (10)

Open a New Ticket

Please fill in the form below to open a new ticket.

| August 2024 | | | | | | | September 2024 | | | | | | |
|-------------|----|----|----|----|----|----|----------------|----|----|----|----|----|----|
| Su | Mo | Tu | We | Th | Fr | Sa | Su | Mo | Tu | We | Th | Fr | Sa |
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 |
| 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 |
| 29 | 30 | 31 | | | | | 29 | 30 | | | | | |

Today

Request for Alveo U55C Development Board (IST)

Request for Versal VCK5000 Development Board (IST)

Step-3: After selecting the date, then click on 'Create Ticket'